



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

BIBDATASHEET

CONFIRMATION NO. 3229

Bib Data Sheet

SERIAL NUMBER 10/627,468	FILING DATE 07/25/2003 RULE	CLASS 438	GROUP ART UNIT 2812	ATTORNEY DOCKET NO. MI22-2348
-----------------------------	-----------------------------------	--------------	------------------------	----------------------------------

APPLICANTS

Tyler A. Lowrey, Sandpoint, ID;

Luan C. Tran, Meridian, ID;
 Alan R. Reinberg, Westport, CT; D. Mark Durcan, Boise, ID;

** CONTINUING DATA *****

This application is a CON of 09/954,340 09/14/2001 PAT 6,599,800
 which is a CON of 09/389,532 09/02/1999 PAT 6,312,988

** FOREIGN APPLICATIONS *****

JB **None**

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/18/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 11	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				

Verified and Acknowledged *JB* Examiner's Signature *JB* Initials

ADDRESS
021567
WELLS ST. JOHN P.S.
601 W. FIRST AVENUE, SUITE 1300
SPOKANE , WA
99201

TITLE

Methods of forming capacitors, methods of forming capacitor-over-bit line memory circuitry, and related integrated circuitry constructions

FILING FEE RECEIVED 750	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
-------------------------------	---	---